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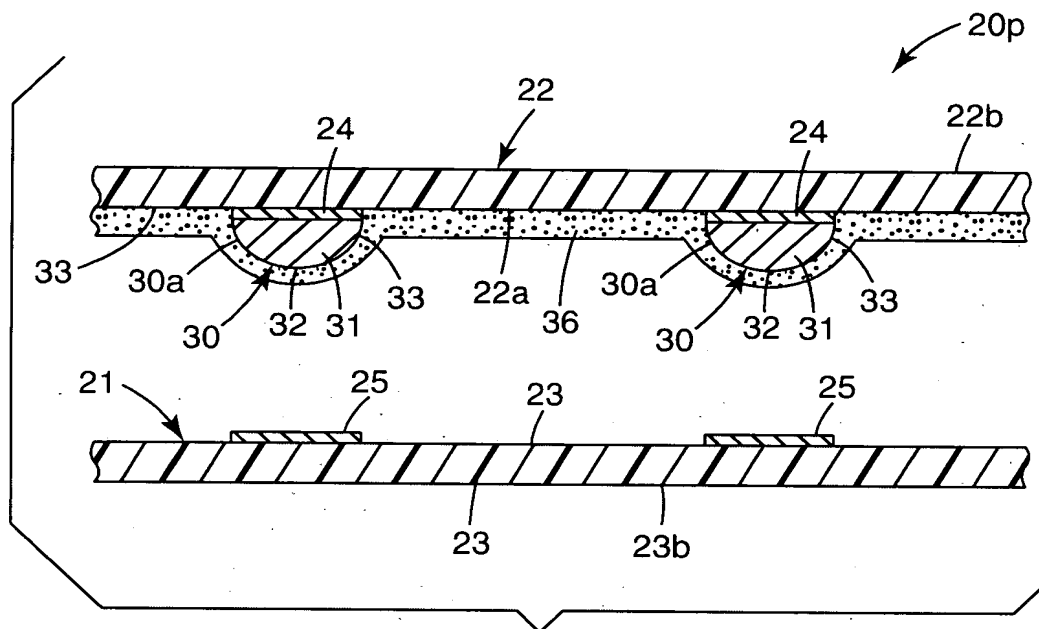


Fig. 1a

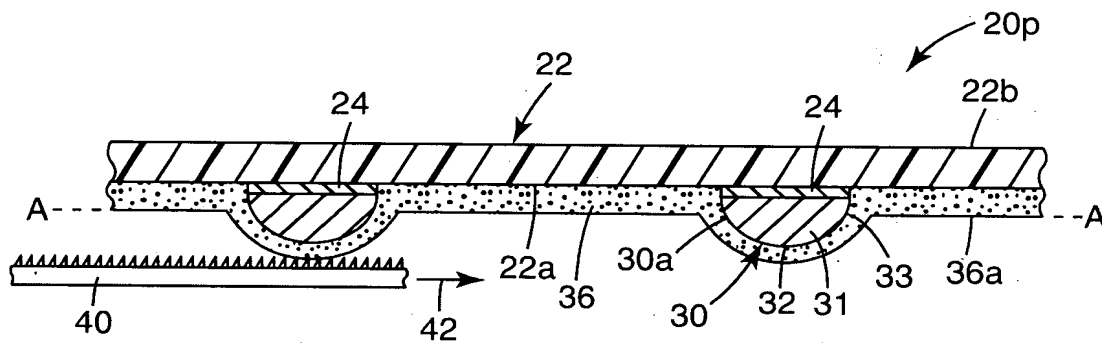


Fig. 1b



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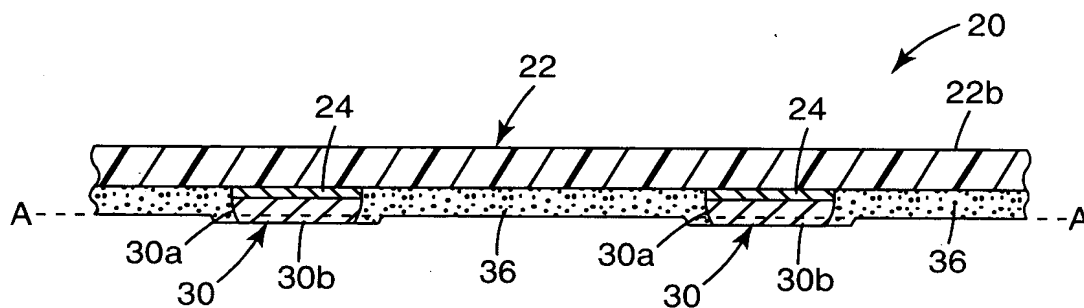


Fig. 1c

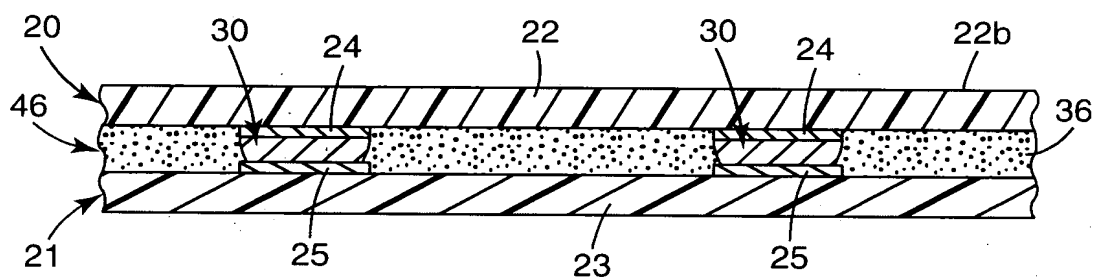
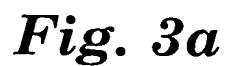
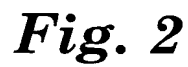
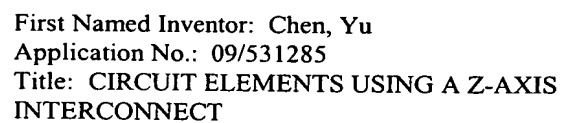


Fig. 1d





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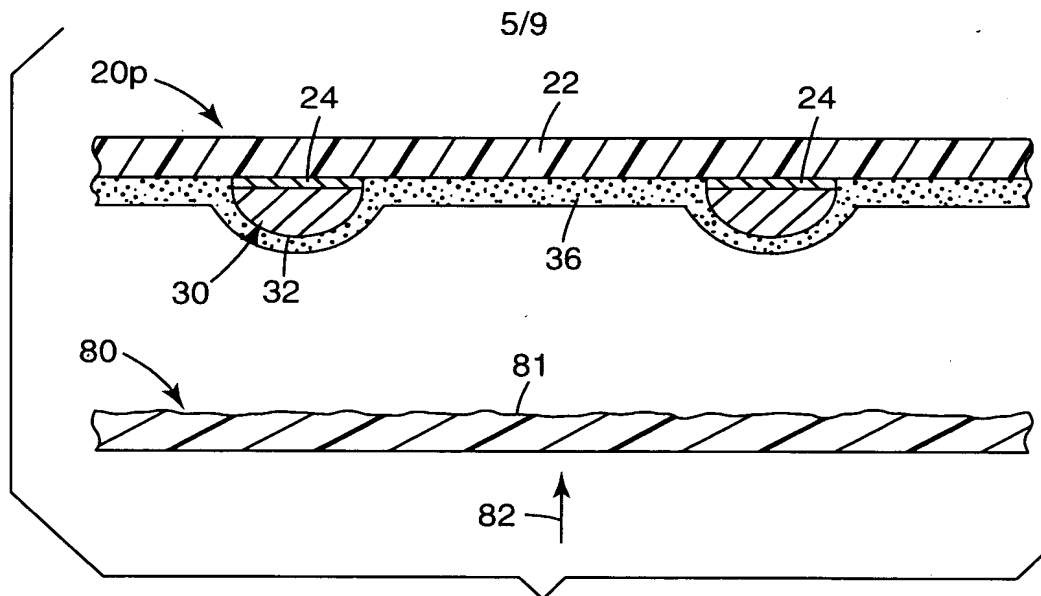


Fig. 4a

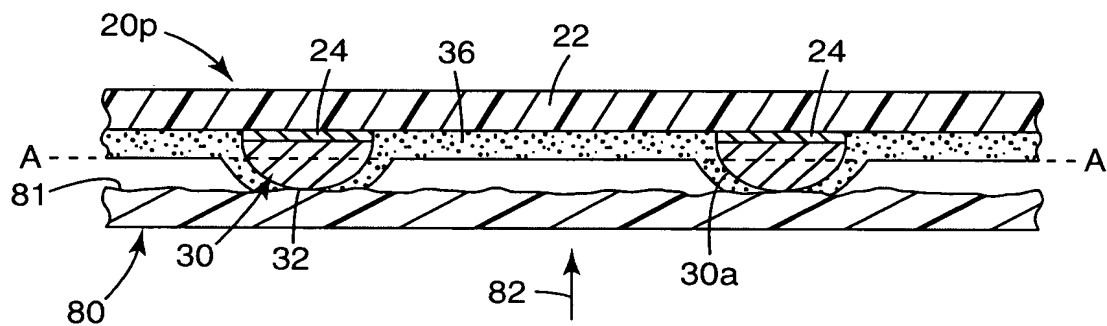


Fig. 4b

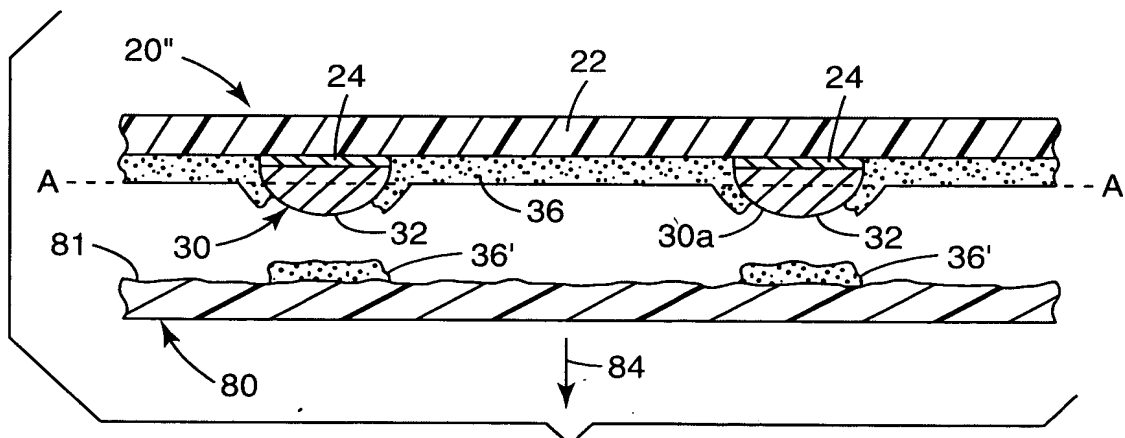


Fig. 4c

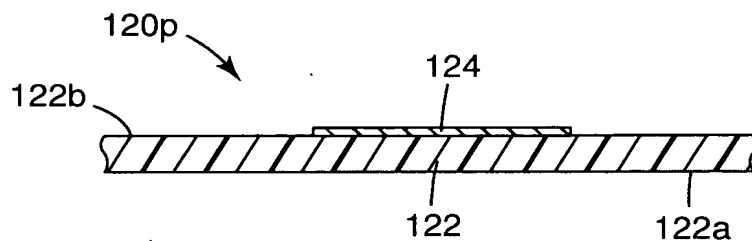


Fig. 5a

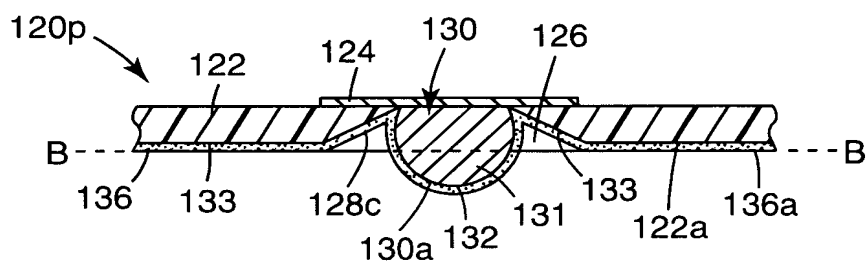


Fig. 5b

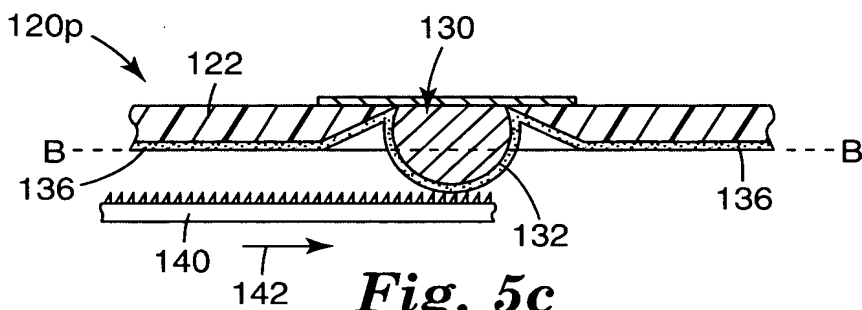


Fig. 5c

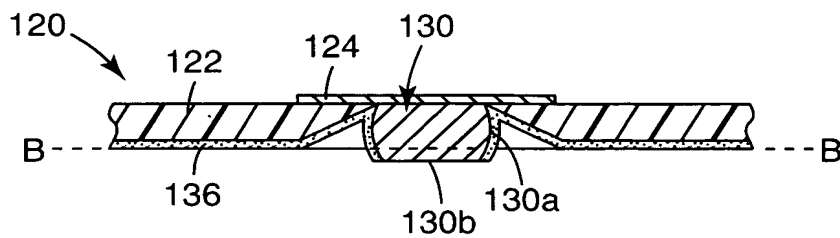


Fig. 5d



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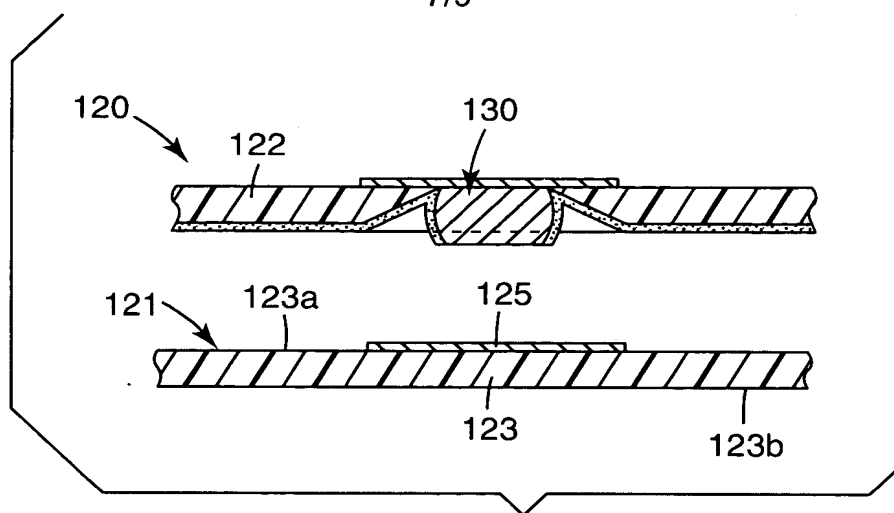


Fig. 5e

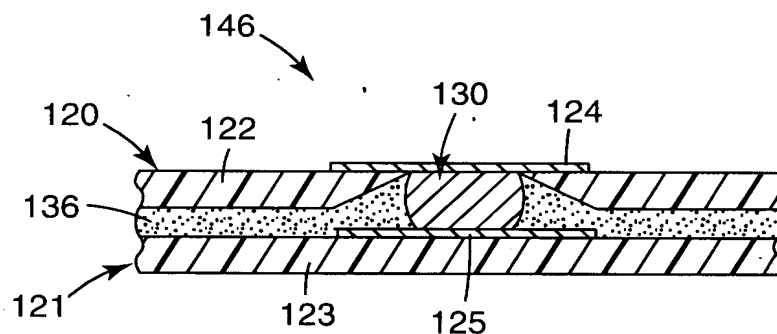


Fig. 5f

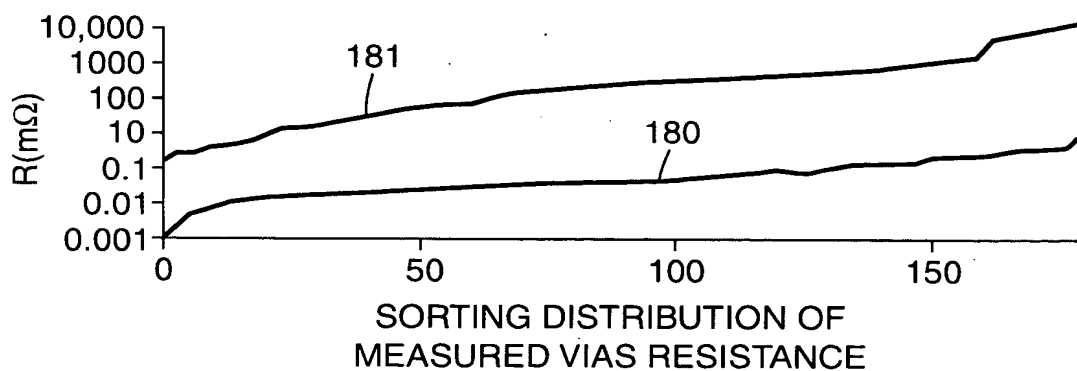


Fig. 7

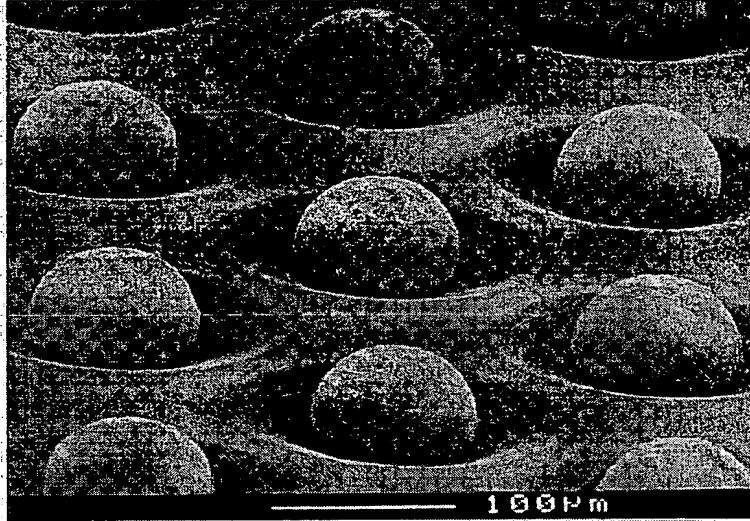


Fig. 6a

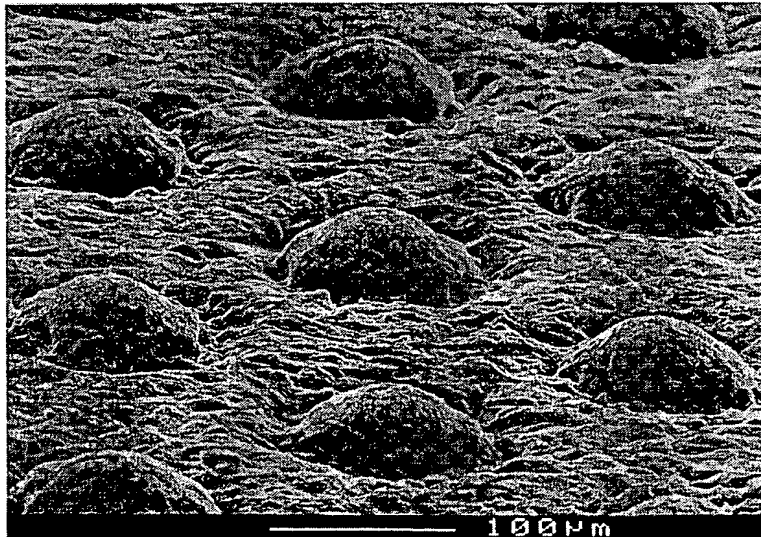


Fig. 6b



First Named Inventor: Chen, Yu
Application No.: 09/531285
Title: CIRCUIT ELEMENTS USING A Z-AXIS INTERCONNEC

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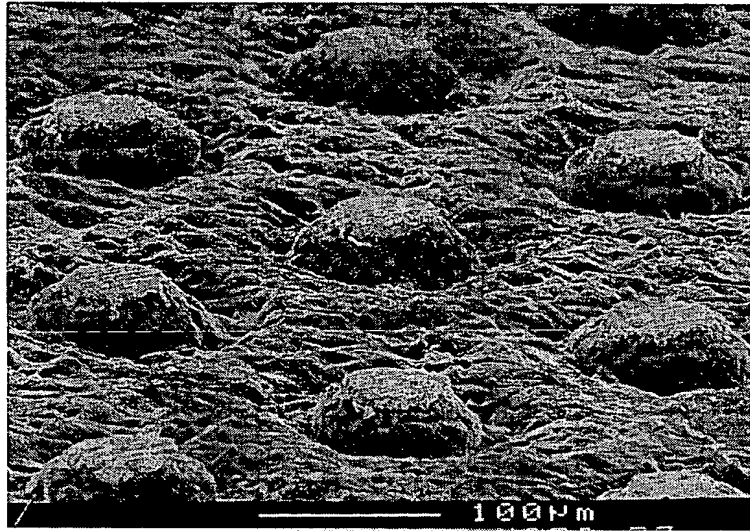


Fig. 6c